

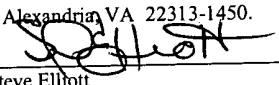


PATENT
Docket No. CA7010223001

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

Date of Deposit: September 29, 2003

I hereby certify that this paper or fee is being deposited with the United States Postal Service as first class mail in an envelope on the date indicated above and is addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Steve Elliott

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

ROBERTSON, William H. et al.

Serial No.: 10/600,999

Filed: June 20, 2003

For: METHOD AND SYSTEM FOR
FACILITATING ELECTRONIC CIRCUIT
AND CHIP DESIGN USING REMOTELY
LOCATED RESOURCES

)
) **Group Art Unit:** Not yet assigned
)

)
) **Examiner:** Not yet assigned
)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 CFR §§ 1.97 and 1.98, the items identified in this Information Disclosure Statement ("IDS") are brought to the attention of the Office. The items are listed on the attached form PTO/SB/08A (08-00). Copies of the items listed are enclosed herewith.

The items identified in this IDS may or may not be "material" pursuant to 37 CFR § 1.56. The submission thereof by Applicant is not to be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR § 1.97(h)), or even qualifies as "prior art" under 35 USC § 102 with respect to this invention unless specifically designated by Applicant as such.

INFORMATION DISCLOSURE STATEMENT FILING PROVISION:

☒ This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b), that is (1) within three months of the filing date of the application, which is not a continued prosecution application filed under § 1.53(d) or (2) within three months of entry of the national stage as set forth in 37 CFR § 1.491; or (3) before the mailing of a first Office action on the merits; or (4) before the mailing of a first Office action after filing a request for continued examination under § 1.114. Thus, no fee is required.

☒ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and charge the fee due under 37 CFR § 1.17(p) to the deposit account referenced below.

☐ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and a statement under 37 CFR § 1.97(e) is included below, thus no fee is required.

☐ This IDS is being submitted under 37 CFR § 1.97(c), that is after mailing of a first Office Action on the merits, but before a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311.

☐ The fee due under 37 CFR § 1.17(p) is submitted herewith.

☐ A statement under 37 CFR § 1.97(e) is included below, thus no fee is required. In the event that this IDS is not received before a Final Action or a Notice of Allowance, then Applicant respectfully requests that the Office consider the filing of these papers to be submitted under 37 CFR § 1.97(d) and charge the fee due under 37 CFR § 1.17(p) to the deposit account below.

☐ This IDS is being submitted under 37 CFR § 1.97(d), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee. A statement under 37 CFR § 1.97(e) is included below. The fee due under 37 CFR § 1.17(p) is submitted herewith.

☐ This IDS is being submitted under 37 CFR § 1.97(i), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee.

STATEMENT UNDER 37 CFR § 1.97(e):

☐ Each item contained in this IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS.

☐ No item contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this statement after making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this IDS.


PAYMENT AND/OR AUTHORIZATION TO CHARGE FEES:

- ☐ A check in the amount of _____ is enclosed for the above fee(s).
- ☒ The Commissioner is authorized to charge any fees required by the filing of these papers, and to credit any overpayment to Bingham McCutchen's Deposit Account No. **50-1189**, Docket No./Billing Reference No. **CA7010223001** for the above fee(s).

Dated: September 29, 2003

Respectfully submitted,
BINGHAM McCUTCHEN LLP

By: _____


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Approved for use through 10/31/2002. OMB 0651-0031

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Complete if Known

Application Number	10/600,999
Filing Date	June 20, 2003
First Named Inventor	William H. Robertson et al.
Group Art Unit	
Examiner Name	
Attorney Docket Number	CA7010223001

(use as many sheets as necessary)

Sheet	1	of	2
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[illegible][illegible]Date
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known

Application Number	10/600,999
Filing Date	June 20, 2003
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Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	12	Benini, L. et al., "Distributed EDA tool integration: the PPP paradigm," Proceedings of the International Conference on Computer Design, 10/1996, IEEE, pgs. 448-453	
	13	Blanchard, D., "EDA on the WWW PCB CAD," Printed Circuit Design, Miller Freeman, 02/1998, vol. 15, no. 2, pgs. 34-37	
	14	Brglz, F. et al., "A universal client for distributed networked design and computing", Jun. 2001, IEEE, 401-406	
	15	Chan et al., "WELD - An Environment for Web-Based Electronic Design," Proceedings of the 35 th Design Automation Conference, 05/1998, pgs. 146-151	
	16	Dalpasso, M. et al., "Specification and Validation of Distributed IP-based designs with JavaCAD," 03/1999, IEEE, pgs. 684-688	
	17	Dalpasso, M. et al., "Virtual simulation of distributed IP-based designs", Jun. 1999, IEEE, pp. 50-55	
	18	Dieckman, D. et al., "DISCOE: distributed design and analysis to preserve intellectual property", Sep. 1998, IEEE, pp 57-60	
	19	Geppert, L., "The Rising Tide of Web-Based Tools and Services Augurs the Next Sea Change in Electronic Design IC Design on the World Wide Web," IEEE Spectrum, 06/1998, vol. 35, no. 6, pgs. 45-50	
	20	Konduri et al., "A Framework for Collaborative and Distributed Web-Based Design," Proceedings of the 36 th Design Automation Conference, 06/1999, pgs. 898-903	
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	24	Orfali, Robert et al., "The essential client/server survival guide", 1996, Katherine Showalter, 2nd ed., pp 228-233 & 476	
	25	Perrin, B., "Web-Based Circuit Engineering," Circuit Cellar Ink, 03/1999, no. 104, pgs. 28-30, 33, 35, 37-38	
	26	Rao et al., "Web-Based Network Analysis and Design," ACM Transactions on Modeling and Computer Simulation, 01/2000, vol. 10, issue 1, pgs. 18-38	
	27	Schindler, P. et al., "IP repository, a Web based IP reuse infrastructure," Custom Integrated Circuits, 05/1999, IEEE, pgs. 415-418	
	28	Wen et al., "Concurrent-Simulation-Based Remote IP Evaluation over the Internet for System-On-Chip Design," 14 th International Symposium on System Synthesis, 09/2001, pgs. 233-238	
	29	Wilsey, P., "Web-Based Analysis and Distributed IP," Proceedings of the 1999 Winter Simulation Conference, pgs. 1445-1453	
	30	Zorian, Y., "Test requirements for embedded core-based systems and IEEE P1500," 11/1997, IEEE, pgs. 191-197	

Examiner
Signature

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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